H903MPLB-G02 Board

The H903MPLB-G02 board is a super control unit board for optical access. It is the core for system control and service switching and aggregation, and is used for MA5800-X17/X15/X7 subracks.





Benefits

- Supports active/standby switchover at the control plane
- Supports load sharing mode at the forwarding plane, doubling processing performance
- Supports ISSU, saving the upgrade interruption time
- Supports synchronous Ethernet
- Supports 1588v2 with proper daughter boards

External Interfaces

- Management interfaces
- > CON (RJ-45) RS-232 serial port
- > ETH (RJ-45) 10/100/1000M Base-T maintenance network port
- ESC (RJ-45)RS-485 monitoring serial port
- > **USB**Reserved
- Communication Interfaces
- > TX/RX 0 to TX/RX 3 (4 x SFP+/SFP 10GE/GE ports)
 Used for upstream transmission or cascading

Specifications

Function	
Bandwidth per slot	200 Gbit/s (load balancing mode)
MAC address table	262143
Access ONT	17408 (earlier than V100R022C0x) 34816 (V100R022C0x and later version)
Multicast user	17408
IPv4 routing table	65536
IPv6 routing table	16384
Service port	139264
ND table	16384
ARP table	131072
Maximum Frame Size	2052 bytes 9216 bytes (jumbo frame enabled)
Environment	
Operating temperature	-40° C to +65° C
Power consumption	Static: 65 W
	Maximum: 125 W



In the preceding table, the IPv4 routing table and ARP table share the same storage space. The IPv6 routing table and ND table share the same storage space.